Hall Ticket Number:

Code No. : 11323 N/O

VASAVI COLLEGE OF ENGINEERING (AUTONOMOUS), HYDERABAD

Accredited by NAAC with A++ Grade

B.E. (E.E.E.) I-Semester Main & Backlog Examinations, Jan./Feb.-2024

Circuit Theory

Time: 3 hours

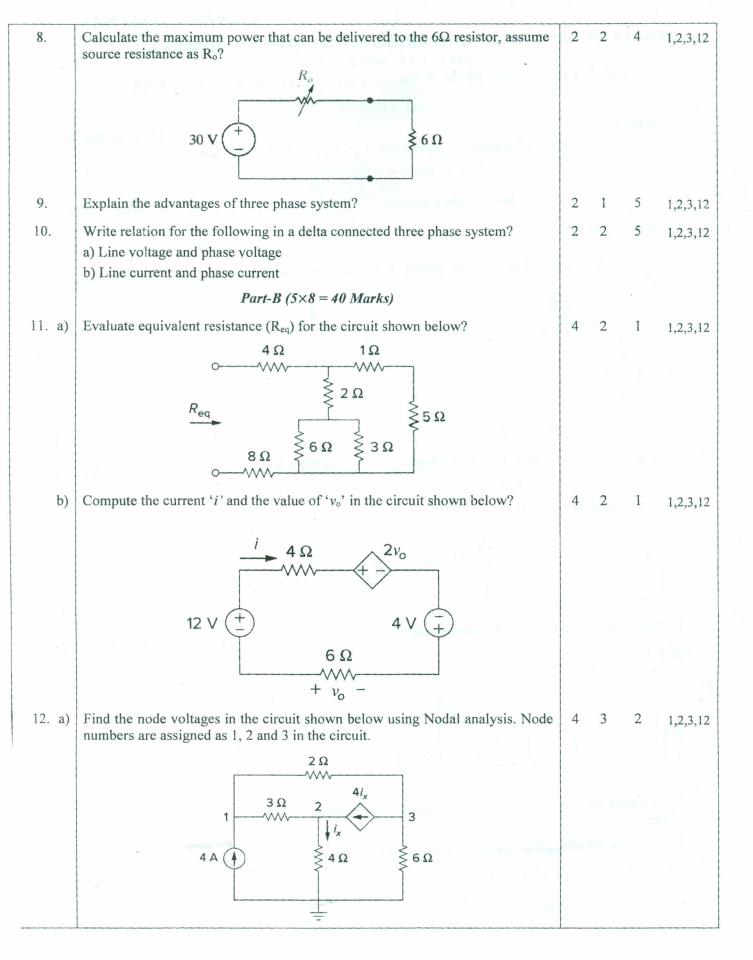
Max. Marks: 60

Note: Answer all questions from Part-A and any FIVE from Part-B

Part-A $(10 \times 2 = 20 \text{ Marks})$

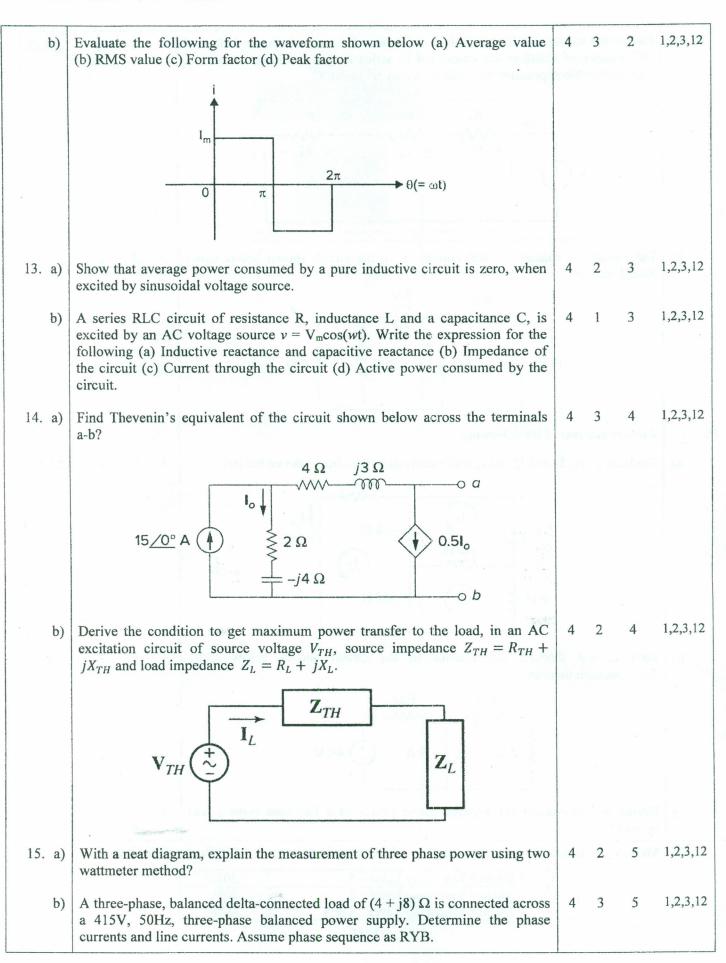
Q. No.	Stem of the question	M	L	СО	РО
1.	Define KCL and KVL?	2	1	1	1,2,3,12
2.	Convert the practical voltage source shown below into practical current source? 2Ω	2	2	1	1,2,3,12
	6 V (+)				
3.	Represent the network in phasor domain, where $v_s(t) = 115\cos(200t)$ Volts.	2	2	2	1,2,3,12
	v _s (+) ∃20 mH				
4.	Evaluate average value of waveform shown below? v(t)	2	3	2	1,2,3,12
		ecciae es los			
5.	Draw the phasor diagram for the following, when excited by sinusoidal voltage waveform	2	3	3	1,2,3,12
6.	Define power factor of an AC circuit?	2	1	3	1,2,3,12
7.	State Superposition theorem.	2	1	4	1,2,3,12

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